Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	152	(703/28).CCLS.	USPAT	OR	OFF	2005/03/17 11:04
L2		(("5006931") or ("5453980") or ("6185732") or ("6243836") or ("6304914") or ("6314530") or ("6639917") or ("6728210") or ("6732307") or ("6421795") or ("6009270") or ("5764885") or ("6732307") or ("5560036") or ("5764885") or ("5809293") or ("5889988") or ("5978902") or ("6314530") or ("6470492")).PN.	USPAT	OR	OFF	2005/03/17 12:06
L3	7	virtual adj microcontroller	USPAT;	OR	ON	2005/03/17 12:07
			EPO; JPO; DERWENT; IBM TDB			
L4	1	(US-6564179-\$).did.	USPAT	OR	OFF	2005/03/17 12:09
L5	29	("4992934" "5283900" "5291614" "5313618" "5329471" "5438672" "5442789" "5450607" "5481684" "5481693" "5748468" "5761516" "5781792" "5790824" "5799169" "5819067" "5872993" "6032247" "6052773" "6163764" "6185522" "6185628" "6223274" "6256725" "6374312").PN. OR ("6564179"). URPN. pod and (microcontroller same (mounted or inserted)) and (xilinx or fpga)	US-PGPUB; USPAT; USOCR USOCR USPAT; EPO; JPO; DERWENT;	OR OR	OFF.	2005/03/17 12:13
L7	49	(microcontroller same (mounted or	USPAT;	OR	ON	2005/03/17 12:14
		inserted)) and (xilinx or fpga)	EPO; JPO; DERWENT; IBM_TDB			
L8	16	7 and emulat\$5	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/17 13:05
L9	2	(("6144327") or ("5202687")).PN.	USPAT	OR	OFF	2005/03/17 14:05
L10	0	pod and emulator and microcontroller and fpga and xilinx and clock and interface	USPAT	OR	ON	2005/03/17 14:05

L11	22	emulator and microcontroller and	USPAT	OR	ON	2005/03/17 14:09
		fpga and xilinx and clock and				
		interface				
L12	1	11 and (emulation or emulator).ti.	USPAT	OR	ON	2005/03/17 14:06

Ref	Hits	Search Query	DBs	Default	Plurals	Time Stamp
#				Operator		
	12	DUT same emulator	USPAT	OR	ON	2005/03/17 17:41